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**INFORMATION DISCLOSURE
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Complete if Known

Application Number	10/612293
Filing Date	June 30, 2003
First Named Inventor	Day, Daniel
Group Art Unit	2877 2117
Examiner Name	Unknown Chung, Phung

Sheet 1 of 1

Attorney Docket No: 884.879US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
/PC/	US-5,396,501	03/07/1995	Sengoku, Shoichiro	371	22	09/30/1992
/PC/	US-6,122,762	09/19/2000	Kim, Ho-Ryong	714	726	09/15/1998
/PC/	US-6,446,230	09/03/2002	Chung, Sung S.	714	726	09/14/1998
/PC/	US-6,449,755	09/10/2002	Beausang, James, et al.	716	5	07/14/2000

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/PC/		"Boundary Scan (JTAG) Tools and Circuit Board Test Solutions", www.acculogic.com/Products/BoundaryScanHome.htm , 2 pages	
/PC/		"Designing for On-Board Programming Using the IEEE 1149.1 (JTAG) Access Port", Intel AP-630 Application Note, Intel order no. 292186-002, available from http://www.intel.com , (November 1996), 14 pages	
/PC/		"Joint Test Action Group from FOLDOC", Available from http://wombat.doc.ic.ac.uk/foldoc/foldoc.cgi?Joint+Test+Action+Group , (11/15/1999), 1 page	
/PC/		"The New Vanguard 330 From Integrated Measurement Systems Offers Cost-Effective Validation of High Performance Logic ICs", www.engineering-uk.co.uk , (04/24/2001), 2 Pages	

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04/27/2007

Substitute Disclosure Statement Form (PTO-1449)

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US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
/PC/	US-5,434,804	07/18/1995	Bock, Robert, et al.	364	579	12/29/1993
/PC/	US-5,583,430	12/10/1996	Dinteman, Bryan J.	324	158.1	04/19/1994
/PC/	US-5,708,773	01/13/1998	Jeppesen, III, James H., et al.	395	183.06	07/20/1995
/PC/	US-6,052,811	04/18/2000	Ahsuri, Roni	714	745	04/15/1997
/PC/	US-6,101,622	08/08/2000	Lesmeister, Gary J.	714	724	04/27/1998
/PC/	US-6,181,151	01/30/2001	Wasson, Will	324	765	10/28/1998
/PC/	US-6,205,560	03/20/2001	Hervin, Mark W.	714	34	02/27/1996
/PC/	US-6,266,793	07/24/2001	Mozdzen, Thomas J., et al.	714	727	02/26/1999
/PC/	US-6,380,730	04/30/2002	Arkin, Brian J., et al.	324	158.1	07/12/2000
/PC/	US-6,392,404	05/21/2002	Kuglin, Philip T.	324	158.1	07/31/2000
/PC/	US-6,392,866	05/21/2002	Dinteman, Bryan J.	361	206	04/18/2000

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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